

Docket No.: P2002,0696

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andreas Jakobs
Filed : Concurrently herewith
Title : Integrated Memory and Method for Setting the Latency in the Integrated Memory

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents,
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

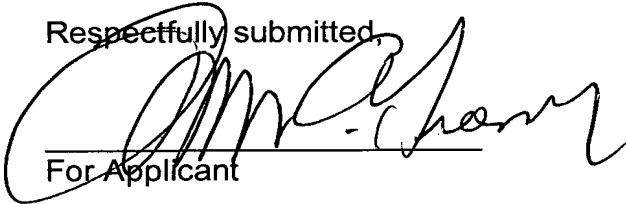
U.S. Patent Application Publication No. 2002/0172085 A1 (Feurle), dated November 21, 2002, corresponds to German Published, Non-Prosecuted Application No. 101 24 278 A1 (Feurle), dated November 28, 2002;

U.S. Patent No. 6,226,724 B1 (Biggs), dated May 1, 2001;

U.S. Patent No. 5,867,422 (John), dated February 2, 1999 and

U.S. Patent No. 5,734,849 (Butcher), dated March 31, 1998.

Respectfully submitted,



For Applicant

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Date: August 27, 2003

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: Appl. No. P2002,0696 <hr/> Applicant Andreas Jakobs <hr/> Filing Date Group Art Unit August 27, 2003			
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EXAMINER INITIALS	PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	2002/0172085 A1	11/02	Feurle		
	B	6,226,724 B1	05/01	Biggs		
	C	5,867,422	02/99	John		
	D	5,734,849	03/98	Butcher		
	E					
	F					
	G					
	H					
	I					

FOREIGN PATENT DOCUMENT							
DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO	
	J	101 24 278 A1	11/02	Germany		X	
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
EXAMINER	DATE CONSIDERED	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		